Fig. 1 Prior Art

Fig. 2

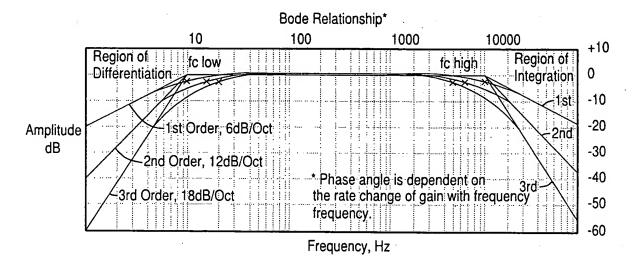
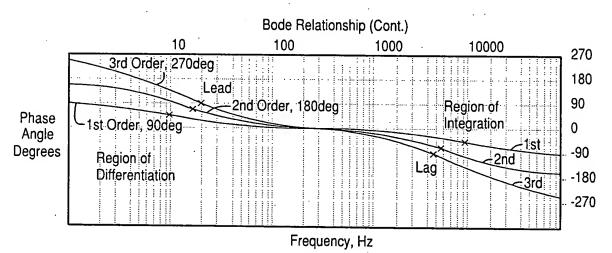


Fig. 3



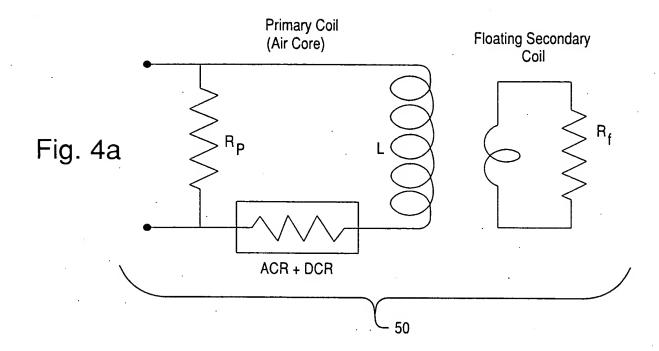
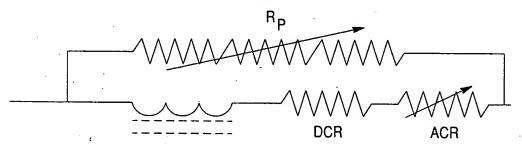


Fig. 4b

Equivalent Circuit



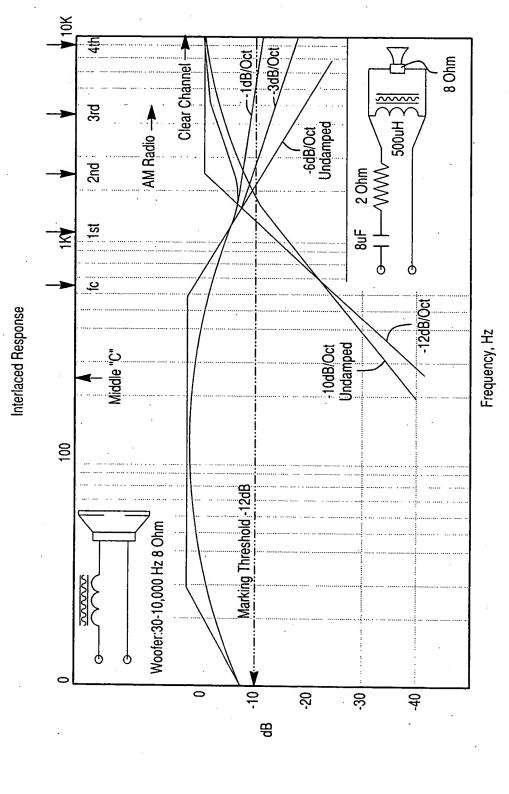


Fig. 5

Fig. 6

Fig. 7

Fig. 7

102

103

119

108

118

109

108

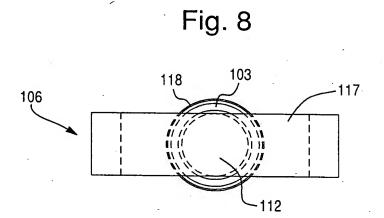


Fig. 9

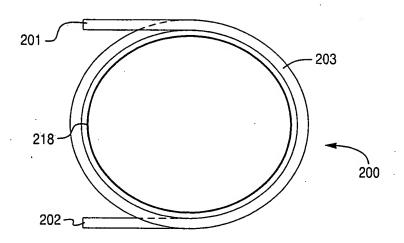


Fig. 10

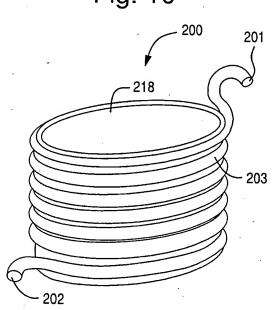


Fig. 11

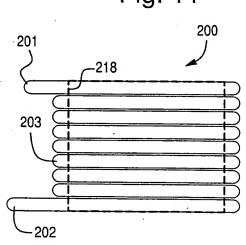


Fig. 12

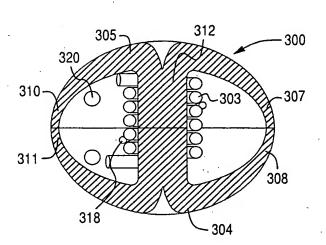


Fig. 13

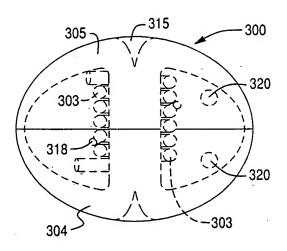
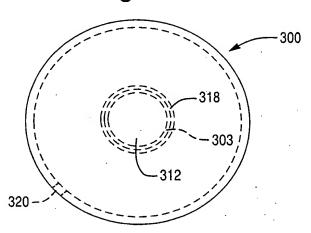


Fig. 14



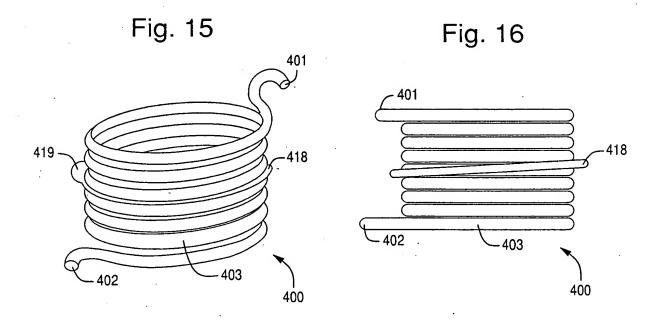


Fig. 17

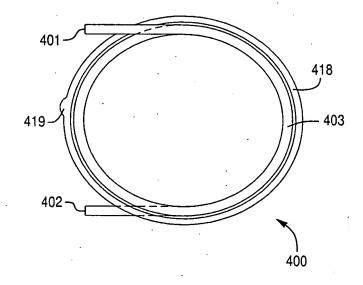


Fig. 18

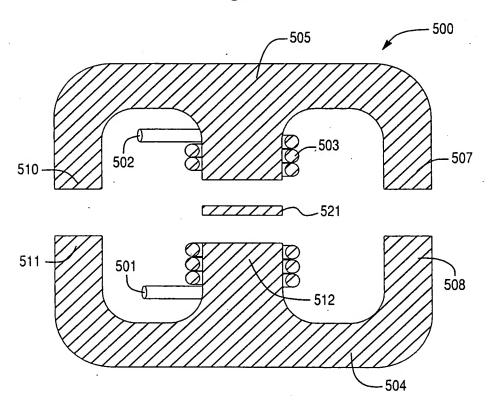


Fig. 19

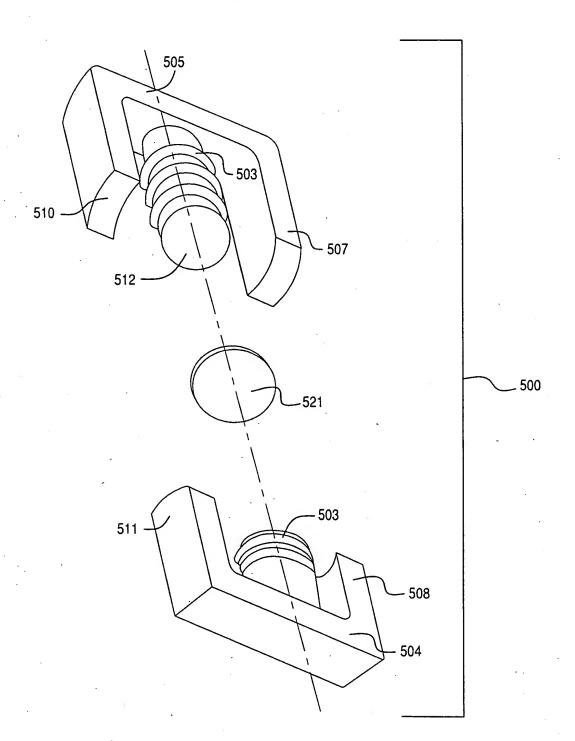


Fig. 20

